

SKiiP 1513 GB172-3DUL V3



2-pack-integrated intelligent Power System

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Features

- SKiiP technology inside
- Trench IGBTs
- CAL HD diode technology
- DC-Link voltage monitoring
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- UL recognized File no. E63532

Typical Applications*

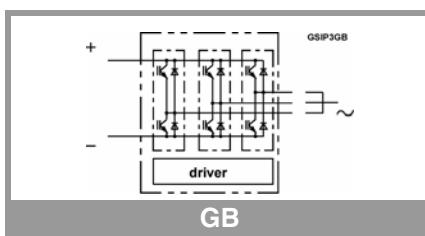
- Renewable energies
- Traction
- Elevators
- Industrial drives

Footnotes

¹⁾ With assembly of suitable MKP capacitor per terminal

Absolute Maximum Ratings		$T_s = 25^\circ\text{C}$ unless otherwise specified		
Symbol	Conditions	Values		Unit
System				
V_{CC} ¹⁾	Operating DC link voltage	1200		V
V_{ISOL}	DC, $t = 1$ s, main terminals to heat sink	5600		V
$I_t(\text{RMS})$	per AC terminal, $T_{\text{terminal}} < 115^\circ\text{C}$	400		A
I_{FSM}	$T_j = 150^\circ\text{C}$, $t_p = 10$ ms, sin 180°	10200		A
I^2t	$T_j = 150^\circ\text{C}$, $t_p = 10$ ms, diode	520		kA²s
f_{out}	fundamental output frequency	1		kHz
T_{stg}	storage temperature	-40 ... 85		°C
IGBT				
V_{CES}	$T_j = 25^\circ\text{C}$	1700		V
I_C	$T_j = 150^\circ\text{C}$	1589		A
	$T_s = 25^\circ\text{C}$	1222		A
	$T_s = 70^\circ\text{C}$	1500		A
I_{Cnom}				
T_j	junction temperature	-40 ... 150		°C
Diode				
V_{RRM}	$T_j = 25^\circ\text{C}$	1700		V
I_F	$T_j = 150^\circ\text{C}$	1336		A
	$T_s = 25^\circ\text{C}$	1017		A
	$T_s = 70^\circ\text{C}$	1250		A
I_{Fnom}				
T_j	junction temperature	-40 ... 150		°C
Driver				
V_s	power supply	13 ... 30		V
V_{IH}	input signal voltage (high)	15 + 0.3		V
V_{ISOLPD}	QPD <= 10pC, PRIM to POWER	1500		V
dv/dt	secondary to primary side	75		kV/μs
f_{sw}	switching frequency	9		kHz

Characteristics		$T_s = 25^\circ\text{C}$ unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	
IGBT					
$V_{CE(sat)}$	$I_C = 900$ A at terminal	$T_j = 25^\circ\text{C}$	1.9	2.4	V
		$T_j = 125^\circ\text{C}$	2.2		V
V_{CE0}		$T_j = 25^\circ\text{C}$	1.00	1.20	V
		$T_j = 125^\circ\text{C}$	0.90	1.10	V
r_{CE}	$I_C = 900$ A at terminal	$T_j = 25^\circ\text{C}$	0.99	1.3	mΩ
		$T_j = 125^\circ\text{C}$	1.4	1.7	mΩ
$E_{on} + E_{off}$	$I_C = 900$ A $T_j = 125^\circ\text{C}$	$V_{CC} = 900$ V	585		mJ
		$V_{CC} = 1200$ V	863		mJ
$R_{th(j-s)}$	per IGBT switch			0.02	K/W
$R_{th(j-r)}$	per IGBT switch			0.018	K/W



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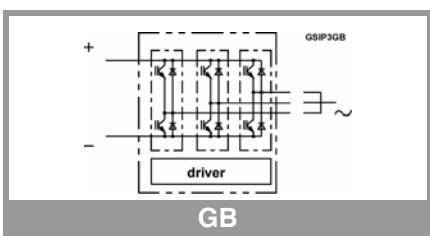
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Footnotes

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Characteristics		$T_s = 25^\circ\text{C}$ unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Unit
Diode					
$V_F = V_{EC}$	$I_F = 900 \text{ A}$ at terminal	$T_j = 25^\circ\text{C}$		2.00	2.15
		$T_j = 125^\circ\text{C}$		1.80	V
V_{FO}		$T_j = 25^\circ\text{C}$		1.1	1.2
		$T_j = 125^\circ\text{C}$		0.8	V
r_F	at terminal	$T_j = 25^\circ\text{C}$		1	1.1
		$T_j = 125^\circ\text{C}$		1.1	$\text{m}\Omega$
E_{rr}	$I_F = 900 \text{ A}$ $T_j = 125^\circ\text{C}$	$V_R = 900 \text{ V}$		108	mJ
		$V_R = 1200 \text{ V}$		128	mJ
$R_{th(j-s)}$	per diode switch			0.038	K/W
$R_{th(j-r)}$	per diode switch			0.058	K/W
Driver					
V_s	supply voltage non stabilized	13	24	30	V
I_{SO}	bias current @ $V_s=24\text{V}$, $f_{sw} = 0$, $I_{AC} = 0$		300		mA
I_s	$k_1 = 42 \text{ mA/kHz}$, $k_2 = 0.00044 \text{ mA/A}^2$	= 300	$+ k_1 * f_{sw}$	$+ k_2 * I_{AC}^2$	mA
V_{IT+}	input threshold voltage (HIGH)	12.3			V
V_{IT-}	Input threshold voltage (LOW)			4.6	V
R_{IN}	input resistance		10		$\text{k}\Omega$
C_{IN}	input capacitance		1		nF
t_{PRESET}	error memory reset time		0.0122		ms
t_{TD}	top / bottom switch interlock time		3		μs
t_{jitter}	jitter clock time		125		ns
t_{SIS}	short pulse suppression time		0.625	0.7	μs
I_{TRIPSC}	over current trip level	1837	1875	1912	A_{PEAK}
T_{trip}	over temperature trip level	110	115	120	$^\circ\text{C}$
V_{DCtrip}	over voltage trip level,		1200		V
$t_{d(on)IO}$	$V_{CC} = 1200 \text{ V}$ $I_C = 900 \text{ A}$ $T_j = 25^\circ\text{C}$	input-output turn-on propagation time		1.4	μs
$t_{d(off)IO}$		input-output turn-off propagation time		1.4	μs
System					
$R_{th(r-a)}$	flow rate=420m ³ /h, $T_a=25^\circ\text{C}$, 500m above sea level			0.0294	K/W
$R_{CC'+EE'}$	measured per switch, $T_s = 25^\circ\text{C}$		0.17		$\text{m}\Omega$
L_{CE}	commutation inductance		4		nH
C_{CHC}	per phase, AC-side		5.1		nF
$I_{CES} + I_{RD}$	$V_{GE} = 0 \text{ V}$, $V_{CE} = 1700 \text{ V}$, $T_j = 25^\circ\text{C}$		3.6		mA
M_{dc}	DC terminals, SI Units	6	8		Nm
M_{ac}	AC terminals, SI Units	13	15		Nm
w	SKiiP System w/o heat sink		2.4		kg
w _h	heat sink		6.2		kg



Isolation coordination acc. to EN 50178 and IEC 61800-5-1

Maximum grid RMS voltage, line-to-line, star point grounded mains	690V+20%
Installation altitude for maximum grid RMS voltage, line-to-line, star point grounded mains	2000m
Maximum transient peak voltage between low voltage circuit and mains	1600V
Pollution degree acc. to IEC 60664-1 outside the moulded power section	2
Overvoltage cat. acc. to IEC 60664-1 for mains	III
Basic isolation	between heat sink and mains; between low voltage circuit and mains
Protection level acc. to IEC 60529	IP00

Environmental conditions acc.to IEC 60721

	Storage	Transportation	Operation stationary use at weather protected locations	Operating ground vehicle installations	Operating ship environment
Climatic conditions	1K2 ₍₁₎	2K2 ₍₁₎	3K3 ₍₁₎	5K1 ₍₁₎	--
Biological conditions	1B1	2B1	3B1	5B1	6B1
Chemically active substances (excluded: salt spray)	1C2	2C1	3C2	5C2	6C2
Mechanically active substances	1S1	2S1	3S1	5S1	6S1
Mechanical conditions	1M3	(4)	3M6 ₍₂₎	5M3 ₍₃₎	6M3
Contaminating fluids	--	--	--	5F1	--

(1) expanded temperature range: -40°C / +85°C. Please note: by operation near 85°C the life time of product is reduced.

(2) 3M7 possible, but due to the mechanic load capacity of external components like DC-Link capacitors limited to 3M6

(3) 5M3 without impact of foreign bodies, stones

(4) no declaration due to customer-specific packing

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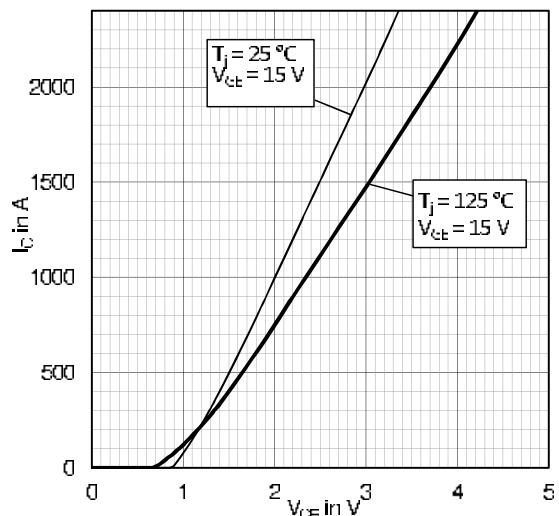


Fig. 1: Typical IGBT output characteristic

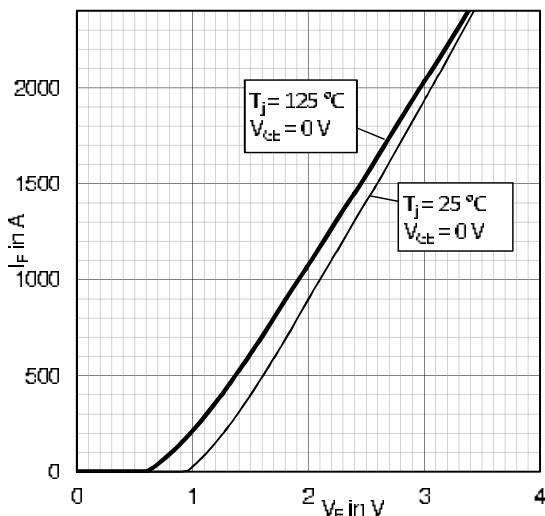


Fig. 2: Typical diode output characteristics

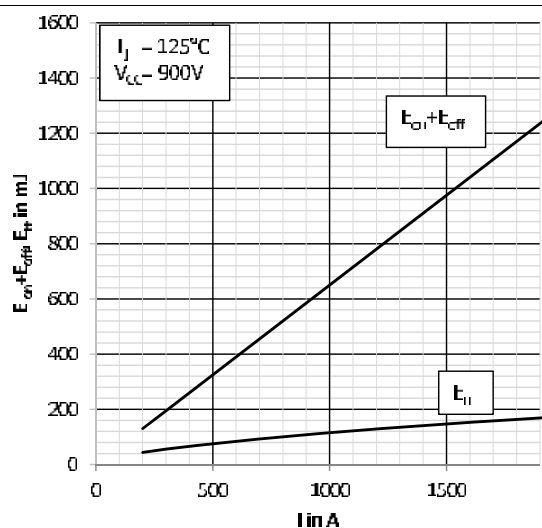


Fig. 3: Typical energy losses $E = f(I_c, V_{cc})$

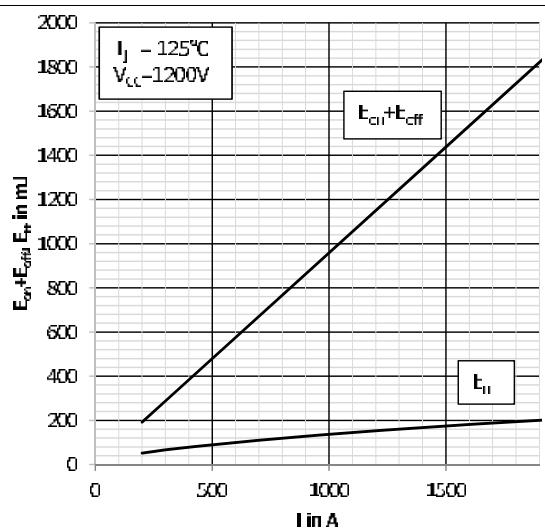


Fig. 4: Typical energy losses $E = f(I_c, V_{cc})$

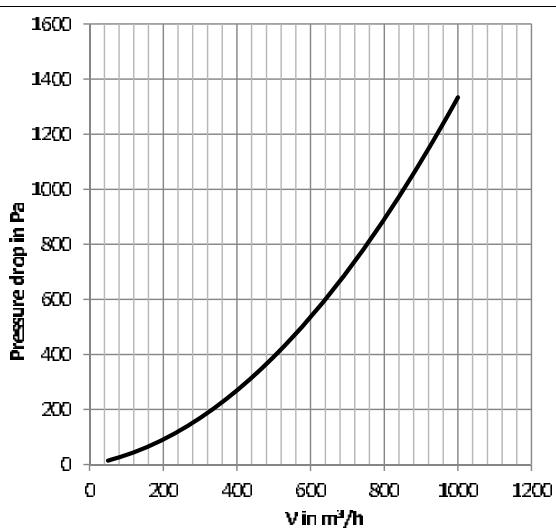


Fig. 5: Pressure drop Δp versus flow rate V

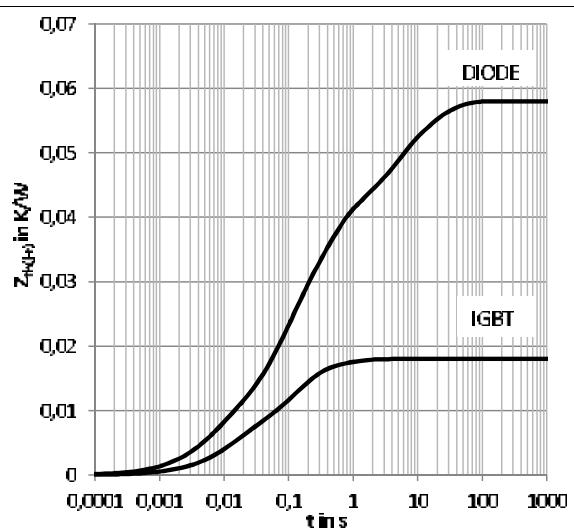
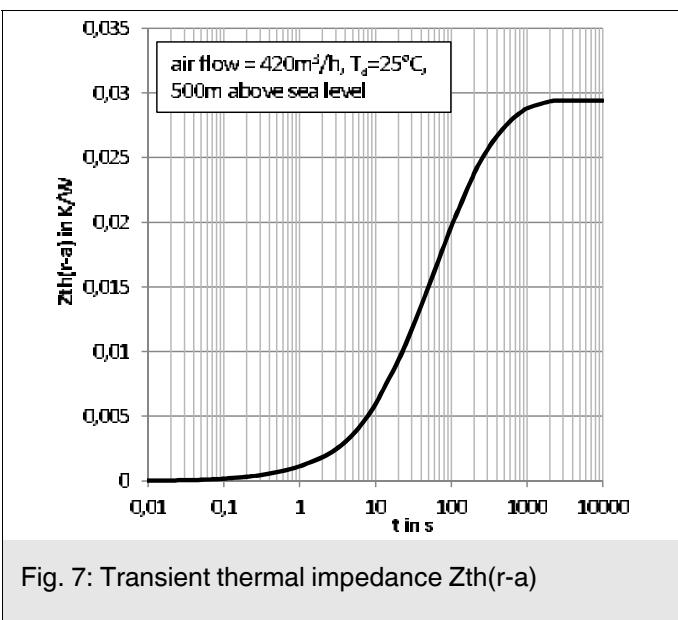


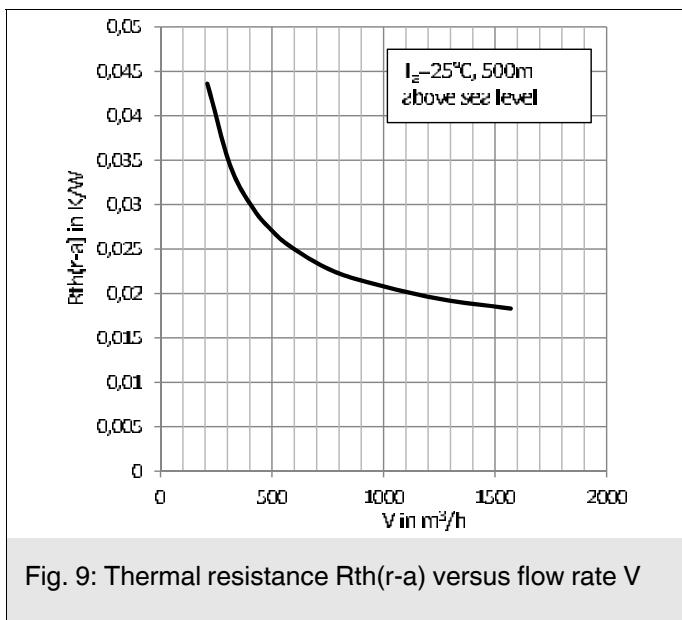
Fig. 6: Transient thermal impedance $Z_{th(j-r)}$

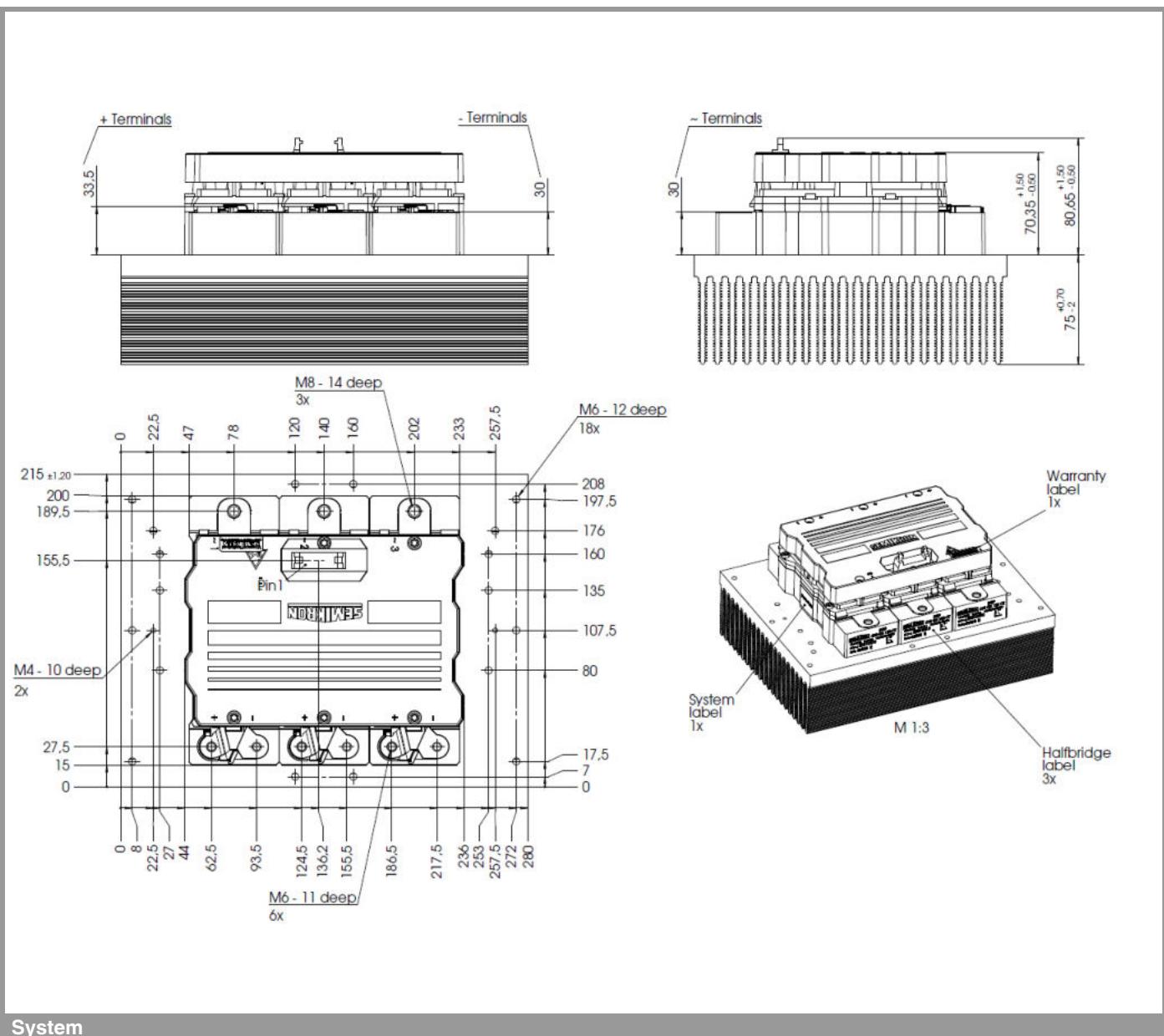
Fig. 7: Transient thermal impedance $Z_{th(r-a)}$

	$R_{th} [\text{K}/\text{W}]$				
	1	2	3	4	5
$Z_{th(r-a)} I$	0,0056	0,0101	0,0023	0,0000	0,0000
$Z_{th(r-a)} D$	0,0070	0,0152	0,0174	0,0112	0,0072
$Z_{th(r-a)}$	0,0005	0,0044	0,0078	0,0100	0,0067

	$\tau_a [\text{s}]$				
	1	2	3	4	5
$Z_{th(r-a)} I$	0,0120	0,1200	0,6000	1,0000	1,0000
$Z_{th(r-a)} D$	0,0060	0,0700	0,3400	4,4000	20,000
$Z_{th(r-a)}$	0,4700	11,000	40,000	105,00	410,00

Fig. 8: Coefficients of thermal impedances

Fig. 9: Thermal resistance $R_{th(r-a)}$ versus flow rate V



System

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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